

Abstract of the Disclosure

A video signal processing apparatus, and more particularly, a horizontal/vertical scanning frequency changing apparatus in an MPEG decoding block for changing a method of scanning a decoded signal using an
5 MPEG algorithm without adding an additional motion detecting memory in a decoding process is provided. According to the present invention, it is possible to reduce material expenses by doubling the horizontal/vertical scanning frequency of the video signal without adding the motion detecting memory and the signal interpolating memory in a process of restoring the
10 signal of the MPEG decoding block and to prevent partial deterioration of picture quality during the signal interpolation depending on the motion information due to the restrictions on the memory capacity.